

Monday, December 19, 2011				
09:00-10:00	Registration			
10:00-11:15	Inauguration (Venue-Auditorium)			
11:15-11:45	HIGH TEA			
11:45-13:00	<b>FIRST Dr. S. C. JAIN MEMORIAL LECTURE</b> Plenary 1 (Venue: Auditorium) Subramanian S. Iyer Scaling in the Third Dimension-Prospects for Silicon-based Interposer and 3D Integration Chair: Vikram Kumar			
13:00-14:00	LUNCH			
Time	VENUE 1 (L-16)	VENUE 2 (L-13)	VENUE 3 (L-15)	VENUE 4 (L-17)
	NT-1	OP-1	OE-1	HF-1
Session Chair	Vinod Tewary	Utpal Das	C. Daniel Frisbie	Ichiro Omura
14:00	<b>Ranjit Pati</b> Molecular Switch Exhibiting Negative Differential Resistance Behavior: Understanding the Underlying Physics	<b>Sanjay Krishna</b> The Infrared Retina: Bioinspired Sensing Using Nanoscale Superlattices and Quantum Dots	<b>Christian Kloc</b> Control of Defects and Dopants in Single Crystals of Organic Semiconductors	<b>Shinichi Nishizawa</b> Wafer Requirement for Future Power Electronics
14:30	<b>Manfred Reiche</b> Characterization of Dislocation-Based Nanotransistors	<b>P. Chakrabarti</b> Ultraviolet Photodetectors based on Metal/ZnO thin-film Contacts	<b>Amlan J. Pal</b> Materials to Devices: From Organic Semiconductors to Organic Electronics	<b>Merlyne De Souza</b> RF Power Amplifier: Pushing the Boundaries of Performance Versus Cost
15:00	<b>Navkanta Bhat</b> Graphene Transistors for CMOS Applications: Opportunities and Challenges	<b>Siddhartha Ghosh</b> Impact Ionization Engineered Type-II Strain Layer Superlattice Avalanche Photodiodes for Linear Mode Photon Counting Applications	<b>Ioannis Kymissis</b> Photocurrent and Noise Analysis as Alternative Approaches to Understanding OFET Behavior	<b>Praveen Shenoy</b> High Voltage Superjunction Technology Development Trends
15:30	<b>R. G. Vaidya</b> (NT-O.01)	<b>Pallabi Pramanik</b> (OP-O.01)	<b>Lay-Lay Chua</b> Structure and Transport Properties of $\pi$ -Stackable Semi-crystalline Polythiophene Semiconductor	<b>K. C. Praveen</b> (HF-O.01)
15:45	<b>N. S. Sankeshwar</b> (NT-O.02)	<b>Tania Das</b> (OP-O.02)		<b>Nayan Patel</b> (HF-O.02)
16:00-16:20	TEA-COFFEE			
16:20-17:05	Plenary 2 (Venue: L-17) C. Jagadish III-V Semiconductor Nanowires for Optoelectronic Device Applications Chair: Philip Klipstein			
Time	VENUE 1 (L-16)	VENUE 2 (L-13)	VENUE 3 (L-15)	VENUE 4 (L-17)
	NT-2	OP-2	OE-2	HF-2
Session Chair	G. Mathad	P. Chakrabarti	Lay-Lay Chua	Shankar Madathil
17:10	<b>Chacko Jacob</b> Growth of Multiwall Carbon Nanotubes – Role of the Catalyst	<b>S. B. Krupanidhi</b> Carrier Transport in InN Based Heterojunctions Grown by Plasma Assisted Molecular Beam Epitaxy	<b>Subhasis Ghosh</b> Unification of Charge Carrier Transport in Small Organic Molecule Based Diode and Field Effect Transistors	<b>Ichiro Omura</b> IGBT: History, Principle and the Future
17:40	<b>R. Prajesh</b> (NT-O.03)	<b>Sunanda Dhar</b> Melt Growth of GaAs <sub>1-x</sub> N <sub>x</sub> (x ≤ 0.01) Epitaxial Layers for Photoconductive Devices	<b>Th. Birendra Singh</b> Charge Transport and Recombination in Small Molecule Solar Cells	<b>Gourab Majumdar</b> Power Device Technologies Catering for Efficient Energy Conversion
17:55	<b>A. Rathi</b> (NT-O.04)			
18:10	<b>Mukesh Singh</b> (NT-O.05)	<b>Akshay Agarwal</b> (OP-O.03)	<b>Ramesh Navan</b> (OE-O.01)	<b>Prashant Singhal</b> (HF-O.03)
18:30-19:15	Plenary 3: (Venue: L-17) Panel Discussion: Semiconductors 2021-Shape of the Future			
19:30	DINNER			

Tuesday, December 20, 2011				
09:00	<b>Plenary 4 (Venue: L-17)</b> <b>Chair: Christian Kloc</b> <b>Ananth Dodbalapur</b> Charge Transport in Polymer and Amorphous Oxide Transistors			
09:45	<b>Plenary 5 (Venue: L-17)</b> <b>Chair: Ritu Sodhi</b> <b>H. Ohashi</b> Advanced Power Devices for Next Generation Power Electronics			
10:30-11:00	<b>TEA-COFFEE</b>			
<b>Time</b>	<b>VENUE 1 (L-16)</b>	<b>VENUE 2 (L-13)</b>	<b>VENUE 3 (L-15)</b>	<b>VENUE 4 (L-17)</b>
	<i>NT-3</i>	<i>OP-3</i>	<i>OE-3</i>	<i>HF-3</i>
<b>Session Chair</b>	<b>M. Husain</b>	<b>B. M. Arora</b>	<b>Ananth Dodbalapur</b>	<b>Ritu Sodhi</b>
11:00	<b>Vinod Tewary</b> Multiscale Modeling of Thermoelastic Characteristics of Graphene	<b>Devki N. Talwar</b> Physics and Applications of Novel Dilute III-V Nitrides	<b>David Cahen</b> Are Proteins Biomolecular Wires?	<b>Tirthajyoti Sarkar</b> Optimized Shielded-gate Trench MOSFET Technology for High-frequency, High-efficiency Power Supplies
11:30	<b>Prabhakar Misra</b> Numerical Modeling and Simulation of Nanobubble Formation on Nanomaterial Substrates and Adsorption of Rare Gas Atoms in Carbon Nanotubes	<b>Philip Klipstein</b> MWIR Photodetectors at SCD with High Operating Temperatures	<b>K. L. Narasimhan</b> Can We Get Information Regarding Disorder Broadening in Organic Semiconductors from Capacitance Spectroscopy?	<b>Sukhendu Deb Roy</b> New Generation MOSFET Design for Battery Powered Portable Applications
12:00	<b>Priya Vashishta</b> Multimillion Atom Simulations of Reactive Nanosystems	<b>Ramesh K. Sonkar</b> (OP-O.04)	<b>Dinesh Kabra</b> Recent Advancement in Hybrid Optoelectronics	<b>Philip Mawby</b> 3C-Silicon Carbide Epitaxy a Novel Approach to Epitaxial Growth for Power Devices
		<b>Jai Verma</b> (OP-O.05)		
12:30	<b>B. N. Chowdhury</b> (NT-O.06)	<b>Ashish Arora</b> (OP-O.06)	<b>K. Sudheendra Rao</b> (OE-O.02)	<b>Dipankar Saha</b> GaN/AlGaN Based High Electron Mobility Transistors
12:45	<b>Dighijoy Nath</b> (NT-O.07)	<b>S. Munawar Basha</b> (OP-O.07)	<b>Farman Ali</b> (OE-O.03)	
13:00-14:00	<b>LUNCH</b>			
14:00-16:00	<b>Poster Session-I (Convocation Ground)</b> <b>Chair: B. M. Arora</b> Nanotechnology; Optoelectronics; and High Frequency/ Power Device			
15:30-16:00	<b>TEA-COFFEE</b>			
16:00-16:45	<b>Plenary 6 (Venue: L-17)</b> <b>Chair: C. Wolden</b> <b>Paul Berger</b> Quantum Tunneling Electronics for Ultra-low Power Scaled CMOS			
<b>Time</b>	<b>VENUE 1 (L-16)</b>	<b>VENUE 2 (L-13)</b>	<b>VENUE 3 (L-15)</b>	<b>VENUE 4 (L-17)</b>
	<i>NT-4</i>	<i>OP-4</i>	<i>OE-4</i>	<i>HF-4</i>
<b>Session Chair</b>	<b>Priya Vashishta</b>	<b>Samit K. Ray</b>	<b>Ioannis Kymissis</b>	<b>Praveen Shenoy</b>
16:50	<b>Dhananjay Kumar</b> Pulsed Laser Deposition Assisted Fabrication of Nano-Dimensional Metallic Particles and Wires for Magnetic and Biological Applications	<b>Anirban Bhattacharya</b> AlGaN based Quantum Wells and Quantum Dots for Ultraviolet Emission	<b>Ali Moazzam</b> Printed Paper Photovoltaics	<b>Dhrubesh Biswas</b> Fabrication and Integration of III-V Hetero-Devices on Silicon Substrate
17:20	<b>S. Maikap</b> Germanium Based Materials for Low Power Nanoscale Resistive Switching Memory Applications	<b>R. Singh</b> Multiphonon Resonant Raman Scattering in Nonpolar GaN Epitaxial Layers	<b>C. Daniel Frisbie</b> New Materials for Printed Polymer Electronics	<b>Ranbir Singh</b> Ultra-Fast/High Voltage SiC Thyristor-based Devices
17:50	<b>G. Mathad</b> Advances in Patterning Technologies for Nano-IC's	<b>Rupak Bhattacharya</b> (OP-O.08)	<b>S. P. Singh</b> Device Physics of High Performing Organic Field Effect Transistor and Organic Photovoltaic Devices Based on PDPP-TNT Polymer	<b>D. S. Rawal</b> (HF-O.04)
18:05		<b>Sumi Bhuyan</b> (OP-O.09)		<b>Souvik Kundu</b> (HF-O.05)
18:20	<b>O. S. Panwar</b> (NT-O.08)		<b>Aarti Mehta</b> (OE-O.04)	<b>Satyaki Ganguly</b> (HF-O.06)
19:30	<b>BANQUET</b>			

Wednesday, December 21, 2011				
09:00	Plenary 7 (Venue: L-17) Chair: <b>K. L. Narasimhan</b>		David Cahen	Assessing Possibilities for Solar Cells by Identifying Basic Limitations to PV Performance
09:45	Plenary 8 (Venue: L-17) Chair: <b>P. Kathirgamanathan</b>		G. Rajeswaran	Advances in Solid State Lighting
10:30-11:00	TEA-COFFEE			
Time	VENUE 1 (L-16)	VENUE 2 (L-13)	VENUE 3 (L-15)	VENUE 4 (L-17)
	<i>MS-1</i>	<i>OP-5</i>	<i>DL-1</i>	<i>PV-1</i>
Session Chair	<b>Aloke K. Dutta</b>	<b>Sunanda Dhar</b>	<b>G. Rajeswaran</b>	<b>David Cahen</b>
11:00	<b>Ken Uchida</b> Thermal-Aware Device Design of Nanoscale MOS Transistors	<b>Samit K. Ray</b> Light Emission Characteristics of Undoped and Rare Earth Doped Germanium Nanocrystals	<b>P. Kathirgamanathan</b> OLEDs for Displays and Lighting: Materials, Processing and Production	<b>Hari Upadhyaya</b> Dye-Sensitised Solar Cells: An Approach towards Low Cost, Energy Generation
11:30	<b>M. B. Patil</b> Modeling and Parameter Extraction of MOSFETs at 77K	<b>S. Chakrabarti</b> Molecular Beam Epitaxy of Quaternary (InAlGaAs) Capped In(Ga)As/GaAs Quantum Dot heterostructure and devices	<b>Tukaram Hatwar</b> Advances in White OLED Technology for Full-color AMOLED Displays and Solid-state Lighting Applications	<b>B. M. Arora</b> III-V Multi-junction Solar Cells: Materials, Devices and Characterization
12:00	<b>Ashwin Seshia</b> The Design of high-Q Silicon MEMS Oscillators	<b>Shivaparasd</b> Epitaxial Growth and Nanostructure Formation on c-Sapphire and Silicon Surfaces	<b>Madhusudan Singh</b> Organic Light Emitting Diodes – Promise, Past Results and Future Trends	<b>J. Woolridge</b> (PV-O.01)
12:30	<b>Kanishka Majumder</b> (MS-O.01)		<b>Deepak Loomba</b> Manufacturing Fab for Solid State Lighting in India	<b>P. K. Basu</b> (PV-O.02)
12:45	<b>Pujarini Ghosh</b> (MS-O.02)			
13:00-14:00	LUNCH			
14:00-16:00	Poster Session-II (Convocation Ground) Chair: <b>Paul Berger</b>		MEMS & Sensors; Modeling & Simulation; Organic Electronics; Displays & Lighting; Photovoltaics and VLSI & ULSI Technology	
15:30-16:00	TEA-COFFEE			
Time	VENUE 1 (L-16)	VENUE 2 (L-13)	VENUE 3 (L-15)	VENUE 4 (L-17)
	<i>MS-2</i>	<i>ME-1</i>	<i>DL-2</i>	<i>PV-2</i>
Session Chair	<b>Ken Uchida</b>	<b>Harsh Harsh</b>	<b>R. K. Jain</b>	<b>Monica Katiyar</b>
16:00	<b>S. Karmalkar</b> Current-voltage Characteristics of Nanowire Junctions	<b>Ajay Agarwal</b> Highly Reproducible Nanowires & Nano-Gap based Sensors	<b>T. N. Ruckmongathan</b> Bit Slice Addressing of Fast Switching Bi-stable Displays and Multi-bit Slice Addressing of State of the Art Active Matrix LCDs	<b>Lars Müller-Meskamp</b> Alternative Transparent Electrodes for Organic Photovoltaics: PEDOT:PSS, Ag Nanowires, and Carbon Nanotubes
16:30	<b>Kaustav Banerjee</b> Graphene Based Green Electronics	<b>Ashwin Seshia</b> Microfabricated Electro-Acoustic Biosensors	<b>Pani Kumar</b> Backlight Dimming Techniques for Large Area LCD TVs	<b>Christine Luscombe</b> Creating Polymer Nanostructures in Organic Photovoltaic Devices
17:00	<b>Ninad Sathaye</b> (MS-O.03)	<b>V. K. Jain</b> Detection of Explosives Using MEMS and Nanotechnology	<b>Pankaj Kr. Uttwani</b> (DL-O.01)	<b>Achintya Dhar</b> Organic Photovoltaics: Paths to Achieve Higher Efficiency
17:15	<b>Monika Bhattacharya</b> (MS-O.04)			
17:30-17:45	TEA-COFFEE			
Time	VENUE 1 (L-16)	VENUE 2 (L-13)	VENUE 3 (L-15)	VENUE 4 (L-17)
	<i>MS-3</i>	<i>ME-2</i>		<i>PV-3</i>
Session Chair	<b>M. B. Patil</b>	<b>Ashwin Seshia</b>		<b>Lars Müller-Meskamp</b>
17:45	<b>Roberto Suaya</b> Some Results Pertaining Electromagnetic Characterization and Model Building for Passive Systems Including TSVs, for 3-D IC's Applications	<b>Amit Agarwal</b> Flow Boiling in Microchannel with Application to Electronic Cooling		<b>Suresh Chand</b> Polymer Photovoltaics- For Bright Future
18:15	<b>R. S. Gupta</b> TCAD Based Performance Assessment of Dual Material Gate AlGaIn/GaN HEMT	<b>Rudra Pratap</b> Design of High-Q Dynamic MEMS Sensors		<b>Ashish Bhatia</b> (PV-O.03)
18:30				<b>Sumita Mukhopadhyay</b> (PV-O.04)
18:45	<b>Arvind Ajoy</b> (MS-O.05)			<b>Asha Yadav</b> (PV-O.05)
19:30	CULTURAL EVENING FOLLOWED BY DINNER			

Thursday, December 22, 2011				
09:45	Plenary 9 (Venue: L-17) Chair: S. Ashok		C. Wolden	Photovoltaic Manufacturing: Present Status, Future Prospects and Research Needs
10:30-11:00	TEA-COFFEE			
Time	VENUE 1 (L-16)	VENUE 2 (L-13)	VENUE 3 (L-15)	VENUE 4 (L-17)
	MS-4	ME-3	VT-1	PV-4
Session Chair	S. Karmalkar	V. K. Jain	S. Qureshi	S. Ashok
11:00	Sayeef Salahuddin Negative Capacitance in a Ferroelectric-Dielectric Heterostructure for Ultra Low Power Computing	Sudhir Chandra Integration of MEMS with Nanostructured Metal-oxide Materials for Improved Sensors for Volatile Organic Compounds	Ashok Srivastava Carbon-Based Electronics - Transistors and Interconnects for Nano Scale Integration	Satyendra Kumar Technological Challenges in PV: From Materials to Systems
11:30	W. H. A. Schilders Numerical Simulation of Organic and Inorganic Semiconductor Devices	Amita Gupta Recent Developments in Deformable Mirrors based on MEMS Technology	Abhishek Dixit Measurement and Analysis of Source/Drain Contact Resistance in FinFETs	Rajeev Jindal CIGS Solar Cells – An Industrial Perspective
12:00	M. P. Ananthram Electrical Transport in Biomolecules	Sarul Malik (ME-O.01)	Anil Kottantharayil Junctionless Field Effect Transistor: Challenges and Prospects in the Deca Nanometer Regime	S. Bhattacharya Temperature Effects on Electrical Parameters of Large Area, Silicon Hetero Junction Solar Cells
12:15		Saakshi Dhanekar (ME-O.02)		
12:30	Ramakrishna Rao Design and Modeling of High Voltage (>1kV) SiC MOSFETs		Anindita Das (VT-O.01)	
12:45			Ankur Arya (VT-O.02)	
13:00-14:00	LUNCH			
Time	VENUE 1 (L-16)	VENUE 2 (L-13)	VENUE 3 (L-15)	VENUE 4 (L-17)
	MS-5	ME-4	VT-2	PV-5
Session Chair	T. N. Ruckmongathan	S. C. Agarwal	Ashok Srivastava	S. S. K. Iyer
14:00	Mohit Bajaj Challenges in Modeling 22nm and Beyond Devices	Vasuda Bhatia Decorating Multi Walled Carbon Nano Tubes with Palladium and Nickel Nanoparticles and Their Applications in Chemical, Gas and Bio-Sensing	S. Barai Optical Lithography Process Development and Process Variability Effects on Printability	Ashish Garg Lifetime and Reliability Issues in Organic Solar Cells
14:30	M. Govindrajan Compact Modeling of SOI Technologies for RF Front-End Module Applications	M. Manivannan MEMS Challenges in Haptics	R. K. Nahar (VT-O.03)	Gangadhar Rao Manufacturing Ecosystem for Photovoltaics and Microelectronics in India
14:45			Sandipan Mallik (VT-O.04)	
15:00		M. Prasad (ME-O.03)	Abhishek Misra (VT-O.05)	Chandan Banerjee (PV-O.06)
15:30	CLOSING SESSION: Poster Awards and Feedback (Venue: L-17)			
16:00	HIGH TEA			